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PTO/SB/05 (11-00)

App. used for use through 10/31/2002. OMB 0651-0032 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE  
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.**UTILITY  
PATENT APPLICATION  
TRANSMITTAL**(Only for new nonprovisional  
applications under 37 CFR 1.53(b))

Attorney Docket No.

854063.601

First Inventor

Davide Patti

Title

HIGH-VOLTAGE INTEGRATED VERTICAL  
RESISTOR AND MANUFACTURING  
PROCESS THEREOF

Express Mail Label No.

EL773169885US

1. ☐ Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original and a duplicate for fee processing)
2. ☐ Applicant claims small entity status.  
See 37 CFR 1.27.
3. ☒ Specification [Total Pages 17]  
(preferred arrangement set forth below)  
- Descriptive title of the Invention  
- Cross Reference to Related Applications  
- Statement Regarding Fed sponsored R & D  
- Reference to sequence listing, a table,  
or a computer program listing appendix  
- Background of the Invention  
- Brief Summary of the Invention  
- Brief Description of the Drawings (if filed)  
- Detailed Description  
- Claim(s)  
- Abstract of the Disclosure
4. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 5]
5. ☒ Oath or Declaration [Total Sheets       ]  
a. ☐ Newly executed (original or copy)  
a. ☐ Copy from a prior application (37 CFR 1.63 (d))  
(for a continuation/divisional with Box 18 completed)  
i. ☐ **DELETION OF INVENTOR(S)**  
Signed statement attached deleting inventor(s) named in  
the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
6. ☐ Application Data Sheet. See 37 CFR 1.76

7. ☐ CD-ROM or CD-R in duplicate, large table or  
Computer Program (Appendix)
8. ☐ Nucleotide and/or Amino Acid Sequence  
Submission (if applicable, all necessary)  
a. ☐ Computer Readable Form (CRF)  
b. ☐ Specification Sequence Listing on:  
i. ☐ CD-ROM or CD-R (2 copies); or  
ii. ☐ paper  
c. ☐ Statements verifying identity of above copies

**ACCOMPANYING APPLICATION PARTS**

9. ☐ Assignment Papers (cover sheet & document(s))
10. ☐ 37 CFR 3.73(b) Statement ☐ Power of  
(when there is an assignee) Attorney
11. ☐ English Translation Document (if applicable)
12. ☐ Information Disclosure ☐ Copies of IDS  
Statement (IDS)/PTO-1449 Citations
13. ☐ Preliminary Amendment
14. ☒ Return Receipt Postcard (MPEP 503)  
Should be specifically itemized)
16. ☐ Request and Certification under 35 U.S.C.  
122(b)(2)(B)(i). Applicant must attach form  
PTO/SB/35 or its equivalent.
17. ☒ Other: Certificate of Express Mail

18. If a **CONTINUING APPLICATION**, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) ☐ of prior application No.       /  
Prior application information Examiner        Group Art Unit:
For **CONTINUATION** or **DIVISIONAL APPS** only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.**19. CORRESPONDENCE ADDRESS**☒ Customer Number or Bar Code Label

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PATENT TRADEMARK OFFICE

or

☐ Correspondence address below

Name	Robert Iannucci			Telephone	
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Name (Print/Type)	Robert Iannucci		Registration No. (Attorney/Agent)	33,514	
Signature				Date	12/07/00

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Davide PATTI – Catania, Italy  
Filed : December 7, 2000  
For : HIGH-VOLTAGE INTEGRATED VERTICAL RESISTOR AND  
MANUFACTURING PROCESS THEREOF

Docket No. : 854063.601

Date : December 7, 2000

Box Patent Application  
Assistant Commissioner for Patents  
Washington, DC 20231

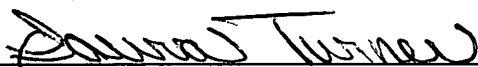
CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Assistant Commissioner for Patents:

I hereby certify that the enclosures listed below are being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 C.F.R. § 1.10, Mailing Label Certificate No. EL773169885US, on December 7, 2000, addressed to Box Patent Application, Assistant Commissioner for Patents, Washington, DC 20231.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

  
\_\_\_\_\_  
Laura Turner

Enclosures:

Postcard  
Form PTO/SB/05  
Specification, Claims, Abstract (17 pages)  
5 Sheets of Drawings (Figures 1-5)

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